

ARUP BHATTACHARYYA

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SHORT PROFESSIONAL SKETCH: 2017

- A. GENERAL FIELDS OF EXPERTISE & INVOLVEMENT:
ELECTRON & ELECTRO-MAGNETIC DEVICES, THIN DIELECTRIC & MAGNETIC FILMS,
INTEGRATED MICRO & NANO ELECTRONICS, GREEN ENERGY, TECHNOLOGY INTEGRATION
AND
TRANSFER, PROPONENT OF SMART SYSTEMS BASED ON INTEGRATED MICRO-ELECTRONICS-
OPTICS-MECHANICS,
TECHNOLOGY INNOVATION, PROGRAM MANAGEMENT, STRATEGIC PLANNING, PRODUCT
DEVELOPMENT, TECHNICAL CONSULTANCY, SCIENCE & TECHNOLOGY INNOVATION FOR
GLOBAL SUSTAINABILITY, EDUCATION & PROMOTION, TECHNICAL COURSE & BOOK
AUTHORSHIP AND VOLANTEERING.
- B. SPECIFIC FIELDS OF CONTRIBUTIONS:
RECOGNIZED AS "PROLIFIC INVENTOR":
URL: https://en.wikipedia.org/wiki/List_of_prolific_inventors
AUTHOR OF THE BOOK: "SILICON BASED UNIFIED MEMORY DEVICES and
TECHNOLOGY", PUBLISHED BY THE CRC PRESS, JULY 2017.
PIONEERING TECHNICAL LEADERSHIP IN SILICON BASED MICRO-ELECTRONICS AND
NANO-ELECTRONICS: TRANSITIONING SILICON-BASED VLSI & ULSI TECHNOLOGY
INTEGRATION FOR BULK & SOI INTEGRATED ELECTRONICS TECHNOLOGY.
DEVELOPMENTS IN THE GENERAL AREA OF DIGITAL MEMORY AND LOGIC TECHNOLOGY
& SPECIFICALLY TO: CCD, BIPOLAR, FET, CMOS, NVM, BICMOS, AND SOC TECHNOLOGY
DEVELOPMENTS & APPLICATIONS.

MULTIPLE HUNDREDS OF “KNOW-HOW” DEVELOPMENTS & INNOVATIONS IN THE ABOVE FIELDS IN THE FORM OF U.S. & INTERNATIONAL PATENTS; ONE OF THE TOP INVENTORS IN THE STATE OF VERMONT, U.S.A.

NEARLY HUNDRED PUBLISHED AND PROPRIETARY TECHNICAL REPORTS INCLUDING OVER FORTY PUBLICATIONS AND PAPER PRESENTATIONS IN TECHNICAL JOURNALS & CONFERENCES.

COURSE PREPARATION, LECTURES & THESIS GUIDANCE FOR GRADUATE STUDENTS IN THE FIELD OF ELECTRONICS BOTH IN USA & WORLDWIDE: WASHINGTON STATE UNIVERSITY, PULLMAN, WA, USA & IEST, W. BENGAL, INDIA.

BOOK AUTHORSHIP IN MEMORY DEVICE AND TECHNOLOGY; TECHNICAL & LEGAL CONSULTANCY FOR U.S. INDUSTRIES.

VOLUNTEERING MISSIONS: UNDP TECHNICAL ADVISOR FOR DEVELOPING WORLD; PROMOTION OF SCIENCE & TECHNOLOGY EDUCATION AMONG WOMEN & MINORITIES IN USA; TECHNICAL COMMITTEE SERVICE IN ECS; INITIATOR, ORGANIZER & CONTRIBUTOR OF FIRST-EVER CONFERENCE IN INDIA ON: “SUSTAINABILITY SOLUTIONS THROUGH TECHNOLOGY”, KOLKATA, INDIA

C1. EXPERIENCE: INDUSTRIAL R&D AND MANAGEMENT, CONSULTING, TEACHING, AUTHORSHIP, VOLUNTEERING:

THE FOXBORO CO.: THIN FILM SENSORS: 3 YEARS

IBM RESEARCH & DEVELOPMENTS: SILICON BULK TECHNOLOGY: INCEPTION TO ULSI: 29 YEARS

MOTOROLA R&D DESIGN & TECHNOLOGY MANAGEMENT: BULK & SOI ULSI: ~4 YEARS

MICRON TECHNOLOGY: ADVISOR, INNOVATOR, FELLOW: TECH. DEVELOPMENT: 6 YEARS

CONSULTING, EDUCATIONAL & SUSTAINABILITY, VOLUNTEERING & AUTHORSHIP: 16 + YEARS

C2. CHRONOLOGY:

2017-2008: TECHNICAL & LEGAL CONSULTANCY, BOOK WRITING, VOLUNTEERING FOR SCIENCE & TECHNOLOGY;

2007-2001: MARI FELLOW & TECHNICAL ADVISOR, MICRON TECHNOLOGY;

2000-1997: SR. MANAGER, R&D, MOTOROLA: TECHNOLOGY APPLICATIONS & SOI

TECHNOLOGY DEVELOPMENT;

1996-1968: IBM BURLINGTON & IBM YORKTOWN: TECHNICAL LEADERSHIP &

MANAGEMENT IN VLSI & ULSI;

1992-1993: IBM CORPORATE FACULTY, ADJUNCT PROFESSOR & ADVISOR TO DEAN OF ENGINEERING, WASHINGTON STATE UNIVERSITY, PULLMAN, WASHINGTON;

1968-1966: TECHNICAL LEADER & INNOVATOR: MAGNETIC THIN FILM SENSORS;

1961-1965: LRSM, UNIV. OF PENNSYLVANIA: Ph.D. INVESTIGATION ON MAGNETOCRYSTALLINE ANISOTROPY;

1960-1956: BACCALORIATE IN ENGINEERING, UNIV. OF CALCUTTA, INDIA (CURRENTLY IEST, INDIA).

D. RECOGNITION:

USA: PROLIFIC INVENTORS; IBM: OUTSTANDING TECHNICAL ACHIEVER; MICRON: FELLOW

INDIA: IEST BOARD OF TRUSTEE, & BY THE EX-PRESIDENT OF INDIA AS A DISTINGUISHED ALUMNI, IEST, INDIA

SENIOR LIFE MEMBER, IEEE, WHO'S WHO WORLD-WIDE, GREEN ENERGY ADVOCATE, GLOBAL SUSTAINABILITY

E. PERSONAL: U.S. CITIZEN, MARRIED, TWO CHILDREN, TWO GRAND CHILDREN.